

REMARKS

Claims 1-21 are pending in the case. All claims rejected. In the present submission, claims 1, 2, 8, 10, 12, 14, 15 and 19 have been cancelled and claims 3, 4, 7, 9, 13, 16, 17, 20 and 21 have been amended. Claims 22 and 23 have been added. Claim 22 replaces independent claim 1. Claims 3, 4, 7, and 9 have been amended to update their dependency to new independent claim 22. Claims 16, 17 and 20 have been amended to update their dependency to claim 13. Reconsideration is respectfully requested.

§101 Rejection

Claims 1-21 are rejected under 35 U.S.C. §101 because the claims are directed to non-statutory subject matter. Claims 1, 2, 8, 10, 12, 14, 15 and 19 have been cancelled and the rejection as to these claims is now moot. In the present submission, independent claim 22 replaces claim 1 and claim 22 recites the practical application accomplished by the claimed invention. Independent claim 13 has been amended accordingly to recite the practical application accomplished by the claimed invention.

More specifically, claim 22 recites “the first parameter comprises an operating temperature associated with the system providing the N-bit digital input value and the second parameter comprises a most significant k bits of a received power parameter, where k is less than N, and the first and second parameters are selected to selectively operate the device for temperature compensation or for non-linear conversion.” Claim 13 has been amended to recite “selecting an indexing parameter from a first parameter and a second parameter using a select input signal, the first parameter comprising an operating temperature associated with the system providing the N-bit digital input value and the second parameter comprising a most significant k bits of a received power parameter, where k is less than N, and the first and second parameters are selected to selectively operate the device for temperature compensation or for non-linear conversion.”

As thus presented, Applicant submits that independent claims 22 and 13 and the dependent claims 3-7, 9, 11, 16-18, 20, 21, and 23 recite statutory subject matter. Withdrawal of the §101 is respectfully requested.

§102(b) Rejection

Claims 1-5, 7-10, 13 and 15-21 are rejected under 35 U.S.C. §102(b) as being anticipated by Kelly (U.S. Patent 5,942,992). Claims 1, 2, 8, 10, 15 and 19 have been cancelled and the rejection as to these claims is now moot. Applicant respectfully traverses the rejection.

In the present submission, claim 22 replaces claim 1. Claim 22 is patentable over Kelly at least by reciting “a **multiplexor** coupled to receive a first parameter and a second parameter”, “the arithmetic logic unit performing the numerical value conversion based on the first equation and **using the N-bit digital input value and the selected coefficient pair to compute** a digital output value in the second unit,” and “the first parameter comprises an **operating temperature** associated with the system providing the N-bit digital input value and the second parameter comprises a **most significant k bits** of a received power parameter” (emphasis added). Kelly does not teach or suggest using a multiplexor to select an indexing parameter from first and second parameters. Kelly describes using only the higher order bits to retrieve coefficients. Furthermore, Kelly provides only the lower order bits to the multiplier for computation. Claim 22 recites using all N-bit of the digital input value to compute the digital output value.

For at least the above reasons, claim 22 is patentable over Kelly. Claims 3-7, 9, 11 and 23, dependent upon claim 22, is patentable over Kelly for at least the same reasons claim 22 is patentable.

Claim 13 is patentable over Kelly at least by reciting “**selecting an indexing parameter from a first parameter and a second parameter** using a select input signal, the first parameter comprising an operating temperature associated with the system providing the N-bit digital input value and the second parameter comprising a most significant k bits of a received power parameter” and “**using the N-bit digital input value** and the selected coefficient pair of slope and offset coefficients **to compute a digital output value**” (emphasis added). For the same reasons stated above with reference to claim 22, claim 13 is patentable over Kelly. Claims 16-18, 20 and 21, dependent upon claim 13, is patentable over Kelly for at least the same reasons claim 13 is patentable.

Withdrawal of the §102(b) rejection of the claims is respectfully requested.

§103(a) Rejection

Claims 6 and 11 are rejected under 35 U.S.C. §103(a) as being unpatentable over Kelly (U.S. Patent 5,942,992). Applicant respectfully traverses the rejection. Claims 6 and 11, dependent upon claim 22, are patentable over Kelly for at least the same reasons claim 22 is patentable. Withdrawal of the §103(a) rejection of the claims is respectfully requested.

CONCLUSION

After the present amendment, claims 3-7, 9, 11, 13, 16-18 and 20-23 are pending in the present application. For the reasons stated above, the application is in condition for allowance and passage of the present case to allowance is respectfully requested. If the Examiner would like to discuss any aspect of this application, the Examiner is invited to contact the undersigned at (408) 382-0480.

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Respectfully submitted,

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